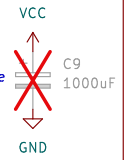


- H1 MountingHole
- H2 MountingHole
- H3 MountingHole

Suspect this much decoupling is excessive. Leaving board space just in case.



This circuit operates to provide a delay effect for SDZ and MUTE.

Both signals are active-low.

To avoid turn-on pops, MUTE should be held low (<2V) for at least 40ms when SDZ goes high.

To avoid turn-off pops, MUTE should be brought low at least 40ms before SDZ.

The capacitor on SDZ provides a delay effect when the 24V enable drops, keeping it above 0.8V for an extra 40ms, while MUTE drops much faster.

The RC + transistor network on MUTE holds MUTE in a low state for approximately 40 milliseconds after 24V enable turns on.

Diode protects transistor from a large (-20V) apparent negative voltage on base pin during turn-off.

This is slightly cheaper than a 555, and as a bonus operates directly off of the source rail. Neat!

Sheet: /		File: amplifier.kicad_sch	
Title: TPA3116 Amplifier with Trigger Input			
Size: A4	Date: 2023-12-20	Rev:	
KiCad E.D.A. kicad 7.0.7	Id: 1/1		